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Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination KAWANO ET AL.	
		Examiner Erik Kielin	Art Unit 2813	Page 1 of 1

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	B	US-4,913,929 A	04-1990	Moslehi et al.	427/564
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, Silicon Processing for the VLSI Era, Vol. 2-Process Integration, Lattice Press: Sunset Beach CA, 1990, pp. 245-252.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.